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THIRD SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENTATTY. DOCKET NO.
1778.1730000APPLICATION NO.
09/702,112FIRST NAMED INVENTOR
Michael Gottlieb JensenFILING DATE
October 30, 2000ART UNIT
2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
RLÉ	AA1	4,068,303	01/1978	Morita	—	—	—
RLÉ	AB1	4,814,975	03/1989	Hirosawa et al.	—	—	—
RLÉ	AC1	5,517,664	05/1996	Watanabe et al.	—	—	—
RLÉ	AD1	5,632,024	05/1997	Yajima et al.	—	—	—
RLÉ	AE1	6,219,774 B1	04/2001	Hammond et al.	—	—	03/25/1998
	AF1						
	AG1				RECEIVED		
	AH1				APR 19 2004		
	AI1				Technology Center 2100		
	AJ1						
	AK1						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes
	AM1						No
	AN1						Yes
	AO1						No
	AP1						Yes
							No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

RLÉ	AR	1	Turley, J., "LSI's TinyRisc Core Shrinks Code Size: Code-Compaction Technique Squeezes MIPS Instructions Into 16 Bits," Microprocessor Report, Microdesign Resources, October 28, 1996, pp. 40-43.
RLÉ	AS	1	Sweetman, D., See MIPS Run, Morgan Kaufmann Publishers, Inc., ISBN 1-55860-410-3, 1999, pp. vii-xiv and 423-425.
RLÉ	AT	1	User's Manual V _R 4121™ 64/32-Bit Microprocessor, NEC Corporation, 1998, pp. 1-19 and 103-131.

EXAMINER	Richard Ellis	DATE CONSIDERED
		5/18/2004

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.